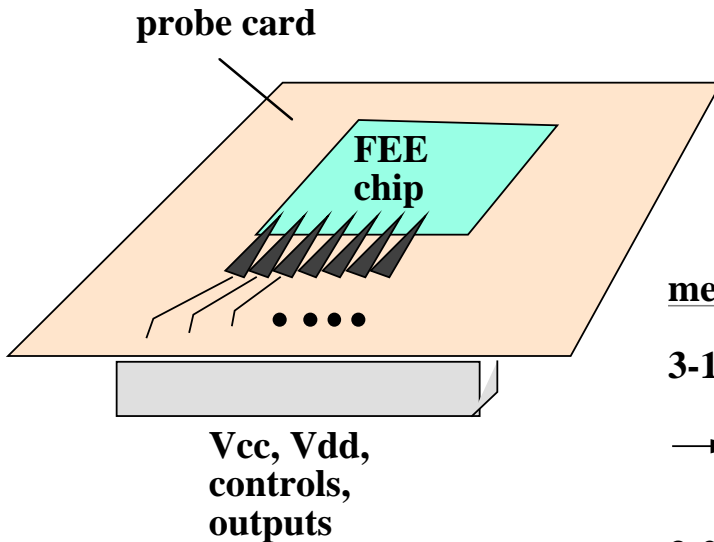


3

### FEE chips

visual inspection  
 charge injection tests with probe card (opt.)



#### measurements:

3-1 visual check at mounting  
 under microscope  
 → check any trace of  
 electrostatic discharge

3-2 data-run

#### time estimate:

3-1 mounting and check 10 min/chip  
 3-2 data-run 20 min/chip (reduced accuracy)

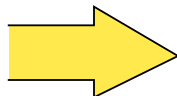
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30 min/chip



16chips/d = (1 module=4chips)/d

2 lines are needed to catch up the  
 throughrate of 2.5 modules/d



Initially, we measure every chip.  
 If the chip failure rate is below 1%, we could set  
 this measurement as optional.